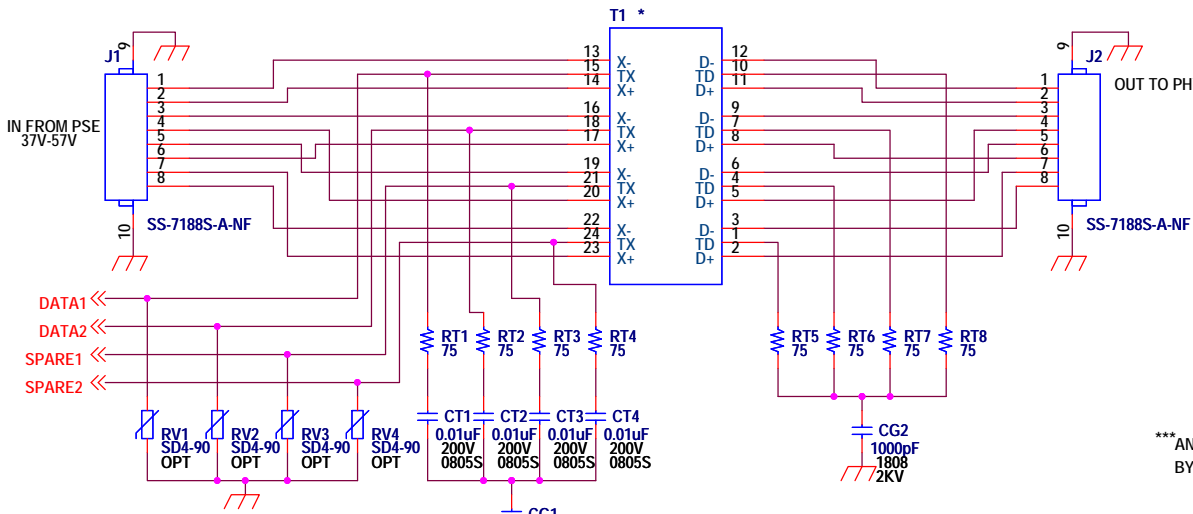
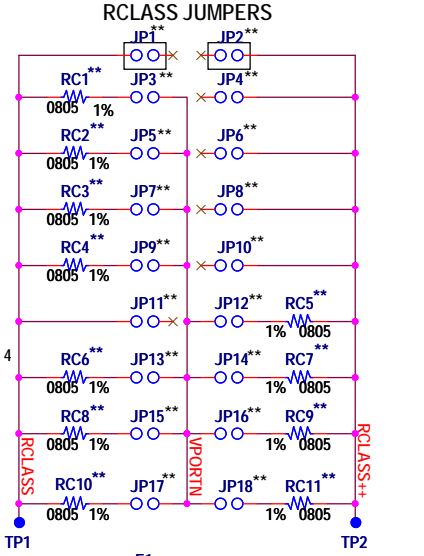


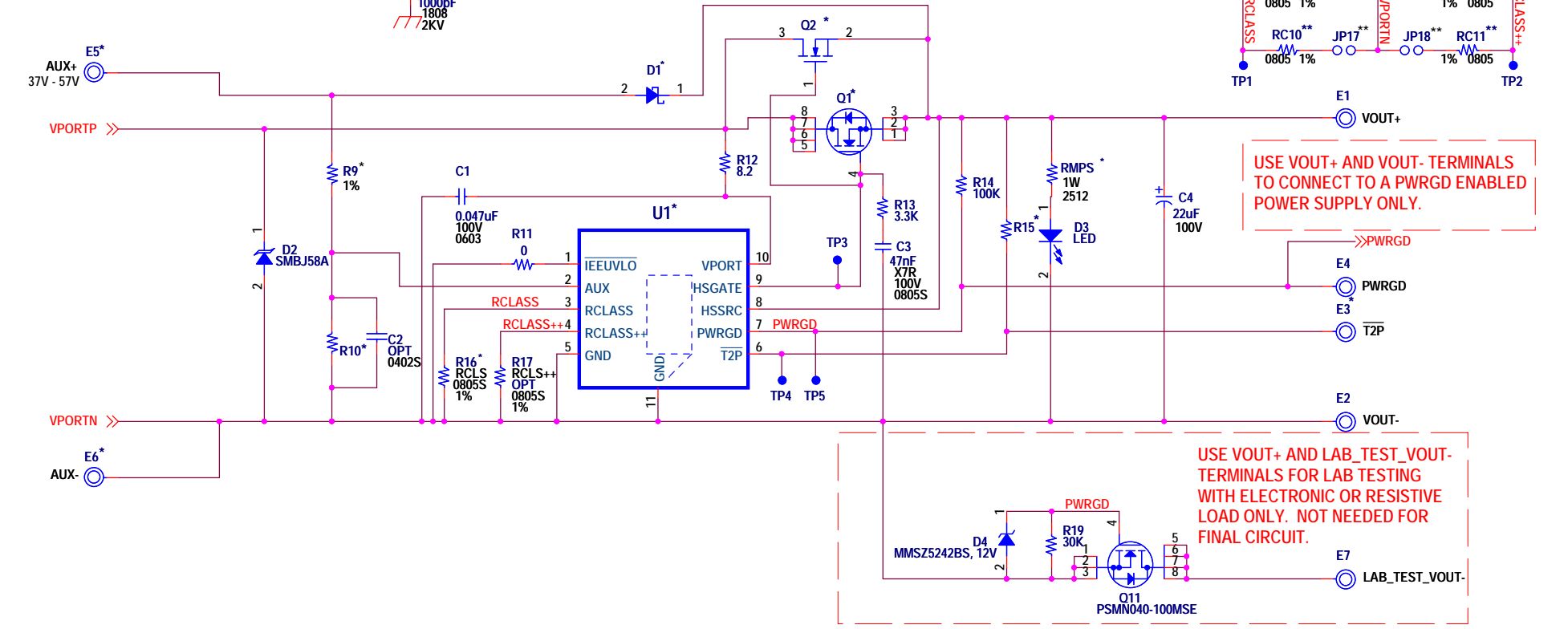
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION	KAUNG H.	06-26-13



JUMPER SELECTIONS				
ASSY	POE CLASS	POWER	CLASS	CLASS ++
A,C	0	13W	JP1	JP2
A,C	1	3.84W	JP3	JP4
A,C	2	6.49W	JP5	JP6
A,C	3	13W	JP7	JP8
A	4	25.5W	JP9	JP10
A	4 ***	38.7W	JP11	JP12
A	4 ***	52.7W	JP13	JP14
A	4 ***	70W	JP15	JP16
A	4 ***	90W	JP17	JP18



*** AN LTPOE ++ PD WILL BE CLASSIFIED AS CLASS 4 BY AN IEEE 802.3 AT COMPLIANT PSE.



USE VOUT+ AND VOUT- TERMINALS TO CONNECT TO A PWRGD ENABLED POWER SUPPLY ONLY.

USE VOUT+ AND LAB_TEST_VOUT- TERMINALS FOR LAB TESTING WITH ELECTRONIC OR RESISTIVE LOAD ONLY. NOT NEEDED FOR FINAL CIRCUIT.

NOTE 1: UNLESS OTHERWISE SPECIFIED
 1. ALL RESISTORS AND CAPACITORS ARE 0603
 2. ALL RESISTORS TOLERANCE RATING ARE 5%
 * SEE OPTION TABLES ON SHEET 2

CUSTOMER NOTICE		APPROVALS				1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only		
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	KAUNG H.			APP ENG.	KAUNG H.	TITLE: SCHEMATIC
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE		SIZE N/A		IC NO.	LT4275XIDD, LT4321IUF DEMO CIRCUIT 2093A	REV. 2
DATE: Wednesday, August 14, 2013				SHEET 1		OF 2		

