Power MOSFET and Schottky Diode

–20 V, –2.5 Å, P–Channel with Schottky Barrier Diode, TSOP–6

Features

- Fast Switching
- Low Gate Change
- Low R_{DS(on)}
- Low V_F Schottky Diode
- Independently Connected Devices to Provide Design Flexibility
- This is a Pb–Free Device

Applications

- DC–DC Converters
- Portable Devices like PDA's, Cellular Phones, and Hard Drives

MAXIMUM RATINGS (T_J = 25° C unless otherwise noted)

Pa	Symbol	Value	Unit			
Drain-to-Source Ve	oltage		V _{DSS}	-20	V	
Gate-to-Source Vo	ltage		V _{GS}	±12	V	
Continuous Drain Current (Note 1)	$\begin{array}{ll} \mbox{Steady State} & T_{A} = 25^{\circ}\mbox{C} \\ T_{A} = 85^{\circ}\mbox{C} \end{array}$		۱ _D	-2.2 -1.6	A	
	t≤5 s	$T_A = 25^{\circ}C$		-2.5		
Power Dissipation			PD	1.0	W	
(Note 1) t ≤ 5 s				1.3		
Pulsed Drain Current t _p = 10 μs			I _{DM}	-7.5	А	
Operating Junction and Storage Temperature			T _J , T _{STG}	–25 to 150	°C	
Source Current (Bo	۱ _S	-0.8	А			
Lead Temperature t (1/8" from case for		urposes	ΤL	260	°C	

SCHOTTKY MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Parameter	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	20	V
DC Blocking Voltage	V _R	20	V
Average Rectified Forward Current	۱ _F	1	Α

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Ambient - Steady-State (Note 1)	$R_{\theta JA}$	125	°C/W
Junction-to-Ambient – t \leq 5 s (Note 1)	R _{0JA}	100	°C/W
Junction-to-Ambient Steady-State (Note 2)	$R_{\theta JA}$	235	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- 1. Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
- Surface Mounted on FR4 Board using the minimum recommended pad size (Cu area = 30 mm² [2 oz] including traces).



ON Semiconductor®

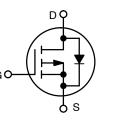
http://onsemi.com

P-CHANNEL MOSFET

V _{(BR)DSS}	R _{DS(on)} Max	I _D Max
-20 V	145 mΩ @ −4.5 V	-2.2 A
201	200 mΩ @ –2.5 V	–1.6 A

SCHOTTKY DIODE

V _R Max	V _F Max	I _F Max
20 V	0.45 V	1.0 A





P-Channel MOSFET

Schottky Diode

MARKING



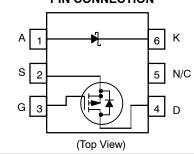
TSOP-6 CASE 318G STYLE 15



TC = Specific Device Code

- M = Date Code
- = Pb-Free Package
 (Note: Microdot may be in either location)

PIN CONNECTION



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

MOSFET ELECTRICAL CHARACTERISTICS (T_J = $25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Test Cor	dition	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _E	₀ = 250 μA	-20			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				14.2		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = -16 V	T _J = 25°C T _J = 85°C			-1.0 -10	μΑ
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V ₀				±100	nA
ON CHARACTERISTICS (Note 3)					•		•
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D	_= −250 μA	-0.5	-0.95	-1.5	V
Gate Threshold Temperature Coefficient	V _{GS(TH)} /T _J		-		3.0		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = -4.5 V	I _D = -2.2 A		90	145	
		V _{GS} = -2.5 V	I _D = -1.6 A		140	200	mΩ
Forward Transconductance	9FS	V _{DS} = -5.0 V,	I _D = -2.2 A		4.5		S
CHARGES, CAPACITANCES AND GATE	RESISTANCE						
Input Capacitance	C _{ISS}	V_{GS} = 0 V, f = 1.0 MHz, V_{DS} = –10 V			400		
Output Capacitance	C _{OSS}				75		pF
Reverse Transfer Capacitance	C _{RSS}				40		
Total Gate Charge	Q _{G(TOT)}				3.8	5.5	
Threshold Gate Charge	Q _{G(TH)}	V _{GS} = -4.5 V, \ I _D = -2	∕ _{DS} = −10 V,		0.5		nC
Gate-to-Source Charge	Q _{GS}	I _D = -2	2.2 A		0.9		
Gate-to-Drain Charge	Q _{GD}	1			1.0		
SWITCHING CHARACTERISTICS (Note 4)							
Turn-On Delay Time	t _{d(ON)}				7.5		
Rise Time	t _r	V _{GS} = -4.5 V, \	∕ _{DS} = −10 V,		6.2		
Turn-Off Delay Time	t _{d(OFF)}	I _D = -1.0 A, F	R _G = 6.0 Ω		14.5		ns
Fall Time	t _f				18.4		
DRAIN-TO-SOURCE CHARACTERISTICS	6						
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V I _D = -0.8 A	$T_J = 25^{\circ}C$		-0.8	1.2	V
Reverse Recovery Time	t _{RR}		•		12		1
Charge Time	T _a	V_{GS} = 0 V, d_{IS}/d_t = 100 A/µs, I_S = –0.8 A			8.0		ns
Discharge Time	T _b				4.0		1
Reverse Recovery Time	Q _{RR}				4.0		nC

Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.5 A		0.32	0.4	V
Forward Voltage		I _F = 1.0 A		0.36	0.45	
Maximum Instantaneous	I _R	V _R = 10 V		0.04	1.0	mA
Reverse Current		V _R = 20 V		0.21	5.0	

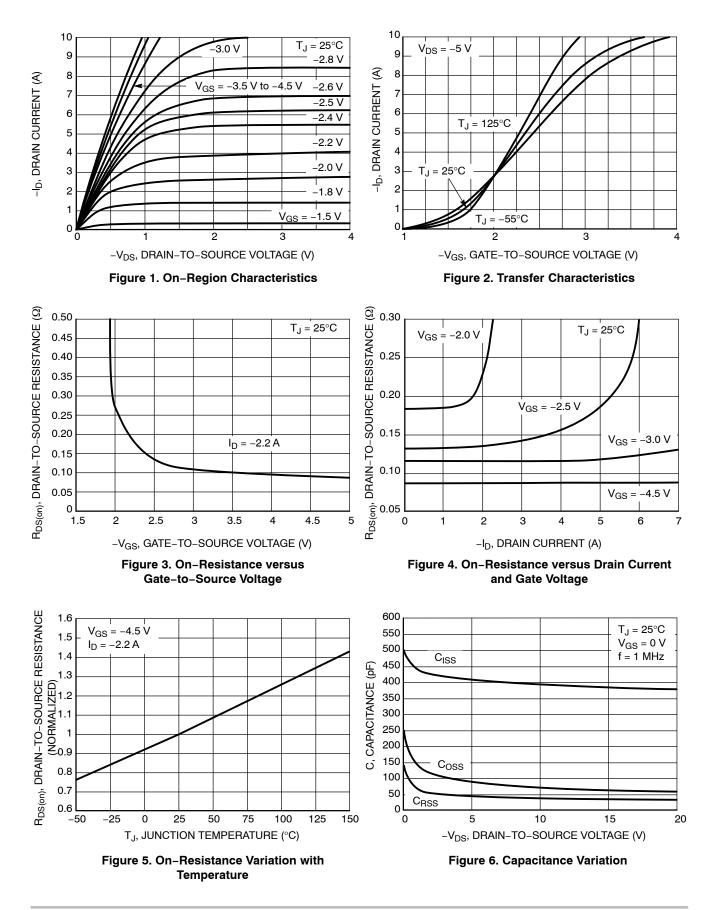
SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS ($T_J = 75^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.5 A		0.27		V
Forward Voltage		I _F = 1.0 A		0.31		
Maximum Instantaneous	Ι _R	V _R = 10 V		0.77		mA
Reverse Current		V _R = 20 V		2.65		

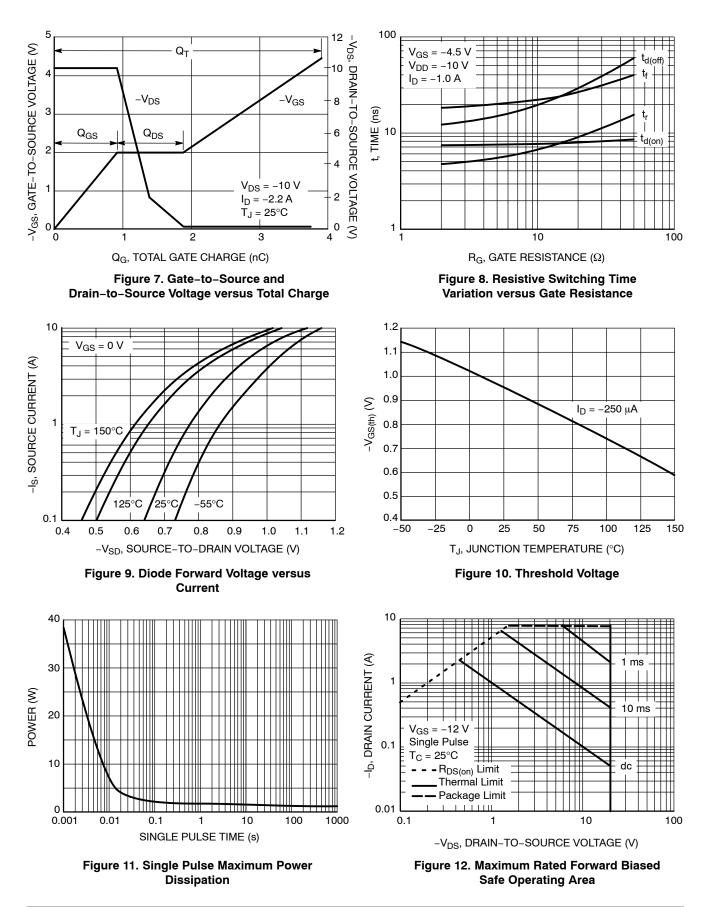
SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = $125^{\circ}C$ unless otherwise noted)

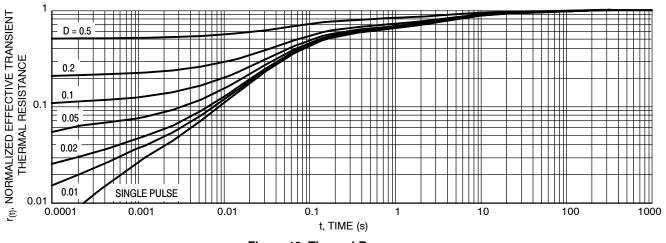
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.5 A		0.22		V
Forward Voltage		I _F = 1.0 A		0.27		
Maximum Instantaneous	I _R	V _R = 10 V		8.75		mA
Reverse Current		V _R = 20 V		37.37		

TYPICAL PERFORMANCE CHARACTERISTICS



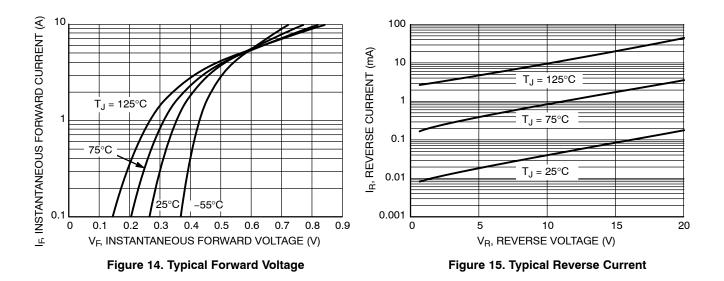
TYPICAL PERFORMANCE CHARACTERISTICS











ORDERING INFORMATION

Device	Package	Shipping [†]
NTGD3147FT1G	TSOP-6	3000 / Tape & Reel
	(Pb-Free)	

⁺For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

onsemi

TSOP-6 CASE 318G-02 ISSUE V DATE 12 JUN 2012 SCALE 2:1 NOTES: D 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. 2 Η MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM З. LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSIONS D 4 ¥ 12 4 GAUGE E1 Е AND E1 ARE DETERMINED AT DATUM H. 5. PIN ONE INDICATOR MUST BE LOCATED IN THE INDICATED ZONE. 2 4 MILLIMETERS М NOTE 5 b DIM MIN NOM MAX 0.90 1.10 DETAIL Z Α 1.00 A1 0.01 0.06 0.10 b 0.25 0.38 0.50 с 0.10 0 18 0.26 D 2.90 3.00 3.10 С Е 2.50 2.75 Α 3.00 $|\cap$ 0.05 E1 1.30 1.50 1.70 e L 0.85 0.95 1.05 0.40 0.20 0.60 Δ1 L2 M 0.25 BSC DETAIL Z 10° 0 STYLE 2: PIN 1. EMITTER 2 2. BASE 1 STYLE 3: PIN 1. ENABLE 2. N/C STYLE 4: PIN 1. N/C 2. V in STYLE 5: PIN 1. EMITTER 2 2. BASE 2 STYLE 6: PIN 1. COLLECTOR 2. COLLECTOR STYLE 1: PIN 1. DRAIN 2. DRAIN COLLECTOR 1 EMITTER 1 3. GATE 4. SOURCE З. 3. R BOOST 4. Vz 3. NOT USED 4. GROUND 3. COLLECTOR 1 4. EMITTER 1 3. BASE 4. EMITTER 4. 5. ENABLE 6. LOAD 5. COLLECTOR 6. COLLECTOR 5. DRAIN 5. BASE 2 5. V in 5. BASE 1 6. V out 6. COLLECTOR 2 6. COLLECTOR 2 6. DRAIN STYLE 10: STYLE 11: STYLE 8: STYLE 9: STYLE 12: STYLE 7 PIN 1. COLLECTOR PIN 1. Vbus PIN 1. LOW VOLTAGE GATE PIN 1. D(OUT)+ PIN 1. SOURCE 1 PIN 1. I/O 2. DRAIN 2 2. GROUND 2. COLLECTOR 2. D(in) 2. DRAIN 2. GND 3. D(in)+ 4. D(out)+ 3. SOURCE 4. DRAIN 3. D(OUT)-4. D(IN)-3. BASE DRAIN 2 3. I/O З. 4 N/C 4 I/O 4 SOURCE 2 5. COLLECTOR 5. D(out) 6. GND 5. 5. VBUS 6. D(IN)+ 5. GATE 1 6. DRAIN 1/GATE 2 5. VCC 6. I/O DRAIN 6. HIGH VOLTAGE GATE 6. EMITTER STYLE 13: PIN 1. GATE 1 STYLE 14: PIN 1. ANODE STYLE 15: PIN 1. ANODE STYLE 16: PIN 1. ANODE/CATHODE STYLE 17: PIN 1. EMITTER 2. SOURCE 2 2. SOURCE 2. SOURCE 2. BASE 2. BASE 3 EMITTER 3 ANODE/CATHODE 3. GATE 2 3 GATE 3 GATE 4. DRAIN 2 4. CATHODE/DRAIN 4. DRAIN 4 COLLECTOR ANODE 5. CATHODE/DRAIN CATHODE 5. SOURCE 1 5. N/C 5. ANODE 5. DRAIN 1 6. CATHODE/DRAIN 6. CATHODE CATHODE COLLECTOR 6. 6. 6. GENERIC RECOMMENDED **MARKING DIAGRAM*** SOLDERING FOOTPRINT* 0.60 XXXAYW= XXX M= 0 o 1LI 6X 3.20 IC STANDARD 0.95 XXX = Specific Device Code XXX = Specific Device Code А =Assembly Location Μ = Date Code Y = Pb-Free Package = Year W = Work Week 0.95 = Pb-Free Package PITCH DIMENSIONS: MILLIMETERS *This information is generic. Please refer to device data *For additional information on our Pb-Free strategy and soldering sheet for actual part marking. Pb-Free indicator, "G" details, please download the ON Semiconductor Soldering and or microdot "•", may or may not be present. Some Mounting Techniques Reference Manual, SOLDERRM/D. products may not follow the Generic Marking. Electronic versions are uncontrolled except when accessed directly from the Document Repository. DOCUMENT NUMBER: 98ASB14888C Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.

 DESCRIPTION:
 TSOP-6
 PAGE 1 OF 1

 onsemi and ONSEMI. are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular

purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation

special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

© Semiconductor Components Industries, LLC, 2019

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent_Marking.pdf</u>. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or indental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification. Buyer shall indemnify and hold onsemi and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs,

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation onsemi Website: www.onsemi.com

ONLINE SUPPORT: <u>www.onsemi.com/support</u> For additional information, please contact your local Sales Representative at <u>www.onsemi.com/support/sales</u>