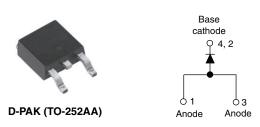
Vishay Semiconductors

High Performance Schottky Rectifier, 3.5 A



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PRODUCT SUMMARY					
Package	TO-252AA (D-PAK)				
I _{F(AV)}	3.5 A				
V _R	60 V				
V _F at I _F	See Electrical table				
I _{RM}	30 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Single die				
E _{AS}	6 mJ				

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-30WQ06FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	3.5	А		
V _{RRM}		60	V		
IFSM	t _p = 5 μs sine	490	А		
VF	3 A _{pk} , T _J = 125 °C	0.53	V		
TJ		-40 to +150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-30WQ06FNHM3	UNITS		
Maximum DC reverse voltage	V _R	60	V		
Maximum working peak reverse voltage	V _{RWM}	80	v		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 133 °C	C, rectangular waveform	3.5		
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	490	A	
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	70		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6.0	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А	

Revision: 03-May-16 1 Document Number: 94732 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>







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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS			
		3 A	T _{.1} = 25 °C	0.61	V	
Maximum forward voltage drop	V _{EM} ⁽¹⁾	6 A	1j=25 C	0.76		
See fig. 1	VFM ("	3 A	T 105 %C	0.53		
		6 A	T _J = 125 °C	0.65		
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C		2	mA	
See fig. 2	IRM (*)	T _J = 125 °C	$V_R = Rated V_R$	30		
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.38	V	
Forward slope resistance	r _t			34.31	mΩ	
Typical junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range	145	pF		
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 5.0			nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/			V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

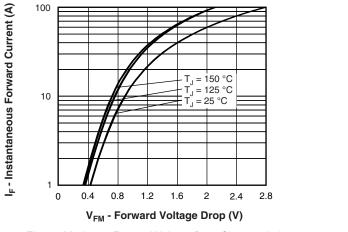
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	4.7	°C/W	
Approvimeto weight			0.3	g	
Approximate weight			0.01	oz.	
Marking device		Case style D-PAK	30WQ0	6FNH	

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

VS-30WQ06FNHM3

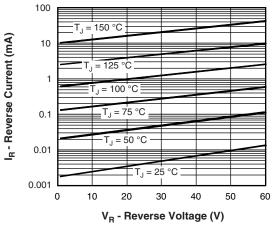
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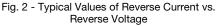


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Fig. 1 - Maximum Forward Voltage Drop Characteristics





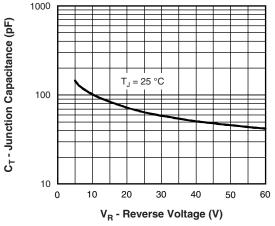


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

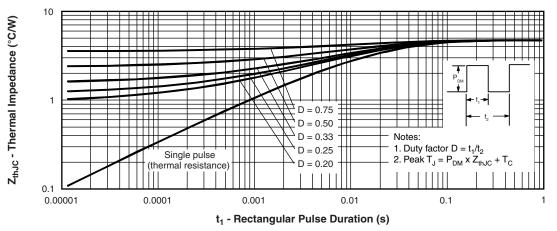
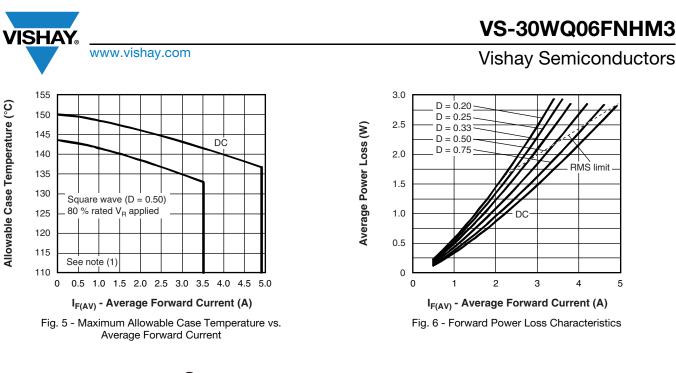
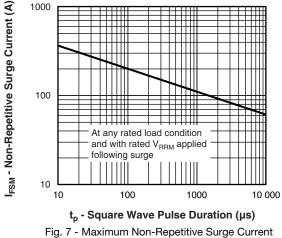


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics





Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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ORDERING INFORMATION TABLE

Device code	VS-	30	w	Q	06	FN	TRL	Н	М3
	1	2	3	4	5	6	7	8	9
[1 ·	- Visł	nay Sen	niconduc	ctors pro	oduct			
[2	- Cur	rent rati	ng (3.5 /	A)				
[3 ·	- Pac	kage id	entifier:					
_		VV =	D-PAK						
[4 ·	- Sch	ottky "C	" series					
[5 -	· Volt	age rati	ng (06 =	= 60 V)				
[6	- FN	= TO-25	52AA (D	-PAK)				
[7.	• N	one = tu	lbe					
		• TI	R = tape	e and ree	el				
		• TF	RL = tap	e and re	eel (left	oriented	d)		
		• TF	RR = tap	be and r	eel (righ	t orient	ed)		
[8 -	• H=	AEC-Q	101 qua	lified				
[9	- Env	rironmer	ntal digit	:				
		M3	= halog	en-free,	RoHS-o	complia	nt, and	terminat	tions lea

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-30WQ06FNHM3	75	3000	Antistatic plastic tube				
VS-30WQ06FNTRHM3	2000	2000	13" diameter reel				
VS-30WQ06FNTRRHM3	3000	3000	13" diameter reel				
VS-30WQ06FNTRLHM3	3000	3000	13" diameter reel				

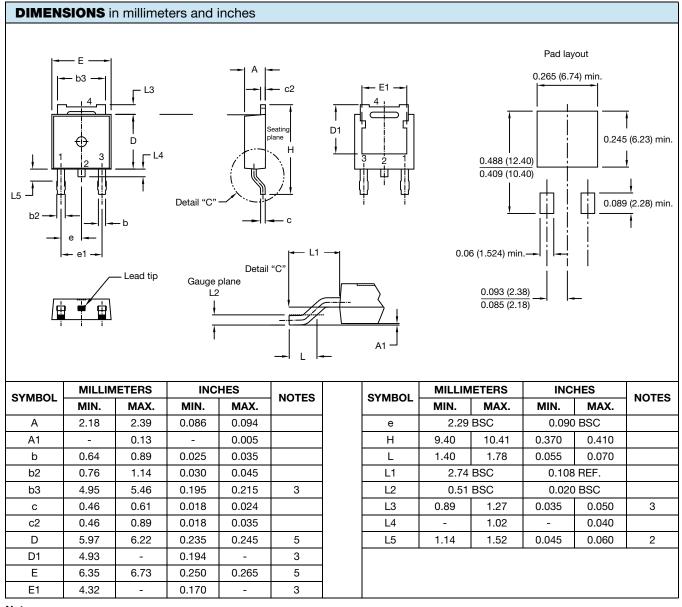
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				
SPICE model	www.vishay.com/doc?95687				

Outline Dimensions



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DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA, except for D1 dimension



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