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Bulletin Date: 7/1/2015		Bulletin Effective Date: 7/1/2015	
Title: Si53019-A01AGM Datasheet Update			
Originator: Kyle Beckmeyer		Phone: 1-408-702-1244	Dept: Timing
Customer Contact: Kyle Beckmeyer		Phone: 1-408-702-1244	Dept: Timing
Bulletin Details			
Description: Silicon Labs is pleased to announce Si53019-A01AGM Data Sheet revision 1.1 with updated pin descriptions in Table30 for pins15-18:			
15	FB_IN	True differential feedback input. Provides feedback signal to the PLL for synchronization with the input clock to eliminate phase error.	
16	FB_IN #	Complementary differential feedback input. Provides feedback signal to the PLL for synchronization with the input clock to eliminate phase error.	
17	FB_OUT	Complementary differential feedback output, provides feedback signal to the PLL for synchronization with input clock to eliminate phase error.	
18	FB_OUT#	True differential feedback output, provides feedback signal to the PLL for synchronization with the input clock to eliminate phase error.	
Reason: Descriptions in the rev1.0 datasheet were incorrect.			
Product Identification: Si53019-A01AGM Si53019-A01AGMR			
This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy. Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at www.silabs.com			
Customer Actions Needed: None			