PCN Nu	mber:	20180215001		PCN Date:	Ма	arch	ו 7, 20)18	
Title:	Datasheet for	r DRV10975, DF	RV10975Z						
Custom	er Contact:	PCN Manager			0	Dep	ot:	Quality Se	ervices
Change	Туре:								
Asse	mbly Site		Design				Wafe	r Bump Site	
Assembly Process			Data Sheet			Wafer Bump Material			
Assembly Materials		s	Part number change			Wafer Bump Process			
Mechanical Specification		cation	Test Site			Wafer Fab Site			
Packing/Shipping/Labeling			Test Process			Wafer Fab Materials			als
EEPROM Settin							OM Setting		
Notification Details									
Description of Change:									
		orporated is anr	nouncing a	n information of	only	no	tificati	on.	
		(s) is being upd							
		istory provides							
	5 5	, ,							
TI is disa	bling Inductive	e AVS in default	EEPROM.						
Narning:	Customers ca	an enable Induc	tive AVS, l	out TI recomm	ends	s no	ot to e	nable featur	e,
because	it is not capab	le of preventing	ı voltage s	urge in all case	es.				
ΓI is prov	iding new typ	ical application	schematic	s for Z (sleep r	node	e) c	levice.		
	(AS								
INS INS	TRUMENTS						DR	V10975, DRV1	0975Z
				SLVSC	CP2C-J	JANU	JARY 2015	-REVISED FEBRUA	RY 2018
Changes	from Revision B ((December 2017) to	Revision C						Page
Added timing information for entering and exiting sleep mode and standby mode									
Added BEMF COMPARATOR hysteresis specification									
Updated Start the Motor Under Different Initial Conditions figure									
Changed the default value for register address 0x27 from 0xFC to 0xF4 in the Default EEPROM Value table									
Deleted the "TI recommends" sentence from the description for address 0x27, bit 3									
Added constraints to recommended external inductor								48	
Changes	from Revision A (March 2017) to Revi	ision B						Page
									-
 Specifi 	ed the drive curren	t as continuous wind	ing current in	he Features					1
 Chang 	ed the r _{DS(on)} maxin	num value from 1 Ω t	$0.4 \ \Omega$ and a	dded typical value i	n the	Elec	ctrical Ch	naracteristics tab	ole 7
Added the internal SPEED pin pulldown resistance to ground parameter to the Electrical Characteristics table							8		
Changed the Step-Down Regulator section							13		
Updated the Motor Phase Resistance section									
Deleted the Inductive AVS Function section									
Changed the default value for register address 0x29 from 0xB7 to 0xB8 in the Default EEPROM Value table									
Added application information for the sleep mode device									
The data	sheet number	will be changin							
Device Family			Change From:			Change To:			
DRV109	75, DRV1097	5Z	SLVSC	P2A			SLVSC	CP2C	
These ch	anges mav be	reviewed at the	e datashee	t links provide	d.				
http://www.ti.com/product/DRV10975									
nttp://w	ww.ti.com/pro	<u>auct/DRV10975</u>							
	for Change:								
To accura	ately reflect de	evice characteris	stics.						

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative): No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

 Changes to product identification resulting from this PCN:

 None.
 Product Affected:

 DRV10975PWP
 DRV10975PWPR
 DRV10975ZPWP

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
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