1. Information Note Basic Data					
1.1 Company	ALLEGRO microsystems	Allegro MicroSystems 955 Perimeter Rd, Manchester, New Hampshire 03103 U.S.A.			
1.2 Information Note No.		20220408TSMC1262			
1.3 Title of IN		A1262 Dual Fab Source: TSMC and UMC			
1.4 Customer		Allegro Authorized Distributor			
1.5 Issue Date		8-Apr-22			

2. Contact								
2.1 Contact Supplier								
2.1.1 Name	Keiko Koike							
2.1.2 Phone	1-603-626-2609	1-603-626-2609						
2.1.3 Email	kkoike@allegromicro.com	kkoike@allegromicro.com						
2.2 Team Supplier (optional)								
2.2.1 Name (optional)	2.2.2 Phone (optional)	2.2.3 Email (optional)						

3. Description						
	Current	New				
Description #1	UMC Fab Supplier	Allegro will be adding Taiwan Semiconductor Manufacturing Compnay (TSMC) as a dual source. TSMC will be the primary wafer fab site.				
Description #2						
Description #3						
Description #4						
Description #5						
Description #6						
Description #7						
Description #8						
Description #9						
Description #10						

4. Reason / Motivation				
4.1 Motivation	Dual Fab source for supply security			
4.2 Additional Explanation (optional)	No change in form, fit, function, or reliability of the product(s) is expected, however validation of equivalence within a specific application is at the discretion of the customer.			

5. Marking of Parts / Traceability					
5.1 Description	No change in marking and traceability (AML Internal Part No. contains fab location)				

	6. Timing / Schedule							
6.1 Intended Start of Delivery	1-Aug-2022	Timing may vary						

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1.3 Title of IN		A1262 Dual Fab Source: TSMC and UMC					
1.4 Customer		Allegro Authorized Distributor					
1.5 Issue Date		8-Apr-22					
	7.	Attachments (e.g. additional documentation, pictures, part list)					

8. Affected Parts									
	8.1 Current				8.2 New (if applicable)				
8.1.1 Customer Part No.		8.1.3 Supplier Internal Part No. (optional)	8.1.4 Package Name	8.1.5 Part Description (optional)	8.1.6 Additional Part Information (optional)		8.1.3 Supplier Internal Part No. (optional)	8.2.4 Package Name	8.2.6 Additional Part Information (optional)
A1262LK-X-T	A1262LK-X-T		OK-4			A1262LK-X-T		OK-4	
A1262LLHLT-T	A1262LLHLT-T		LH-3			A1262LLHLT-T		LH-3	
A1262LLHLX-T	A1262LLHLX-T		LH-3			A1262LLHLX-T		LH-3	

	1. Information Note Basic Data							
1.1 Company	ALLEGR microsyste	O ems	Allegro MicroSystems 955 Perimeter Rd, Manchester, New Hampshire 03103 U.S.A.					
1.2 Information	1.2 Information Note No.		1262					
1.3 Title of IN		A1262 Dual Fab	Source: TSMC a	nd UMC				
1.4 Customer		Allegro Authoriz	zed Distributor					
1.5 Issue Date		8-Apr-22						

Form adopted from ZVEI Revision 5.0

Please note: It is our intention to inform our customer of changes as early as possible. Under Allegro's procedure for product/process change notification, Allegro strives, based on its technical judgment, to provide notification of significant changes that may affect form, fit, function, reliability/durability and processability/manufacturability. However, as Allegro cannot ensure evaluation of product/process changes for each and every application, the customer retains responsibility to validate the impact of a change on its application suitability. If samples are needed for validation of a change, requests may be made via the contact information provided herein. Please contact your Account Manager or Local Sales Representative for any questions.