



Cypress Semiconductor Corporation – An Infineon Technologies Company
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PRODUCT CHANGE NOTIFICATION

PCN:PCN201704

Date: April 22, 2020

Subject: Qualification of a Design Revision for FM25V20A-PG Device

Description of Change:

Cypress announces the qualification of an all-mask design revision for the part number FM25V20A-PG, the 2-Mbit (256K x 8) Serial (SPI) F-RAM qualified in an 8-lead PDIP (300 mils) package. The new design revision incorporates the advantages of Cypress Semiconductor's leading serial non-volatile random-access memory platform, Excelon™, to the current 2-Mbit SPI F-RAM. Migration to the new die revision will result in customers benefitting from continuing product supply and long-term technical support on these products.

There are some updates to certain DC specifications, including a revision in the typical value of the V_{DD} supply current at 25 °C (I_{DD}) at clock frequency $f_{SCK} = 25\text{MHz}$ from 1.3mA to 1.5mA. The updated datasheet is attached to this notification. There is no change to existing marketing part numbers.

Benefit of Change:

This qualification has improved product availability and is consistent with Cypress' product roadmap of offering the latest F-RAM technology and technical support to customers.

Part Numbers Affected: 1

See the attached 'Affected Parts List' file for a list of all part numbers affected by this change. Note that any new parts that are introduced after the publication of this PCN will include all changes outlined in this PCN.

Qualification Status:

This improved die revision has been qualified through a series of tests documented in the Qualification Test Plan QTP#193412. This qualification report can be found as an attachment to this PCN or by visiting www.cypress.com and typing the QTP number in the keyword search window.

Sample Status:

Qualification samples may not be built ahead of time for all part numbers affected by this change. Please review the attached 'Affected Parts List' file for a list of affected part numbers with their associated sample ordering part numbers. If you require qualification samples, please

contact your local Cypress Sales Representative as soon as possible, preferably within 30 days of the date of this PCN, to place any sample orders.

Approximate Implementation Date:

Effective 90 days from the date of this notification or upon customer approval, whichever comes first, all shipments of Commercial, Industrial and Automotive non-PPAP part numbers in the attached file will be designed with new die revision or other approved die revision.

Anticipated Impact:

None anticipated. Products manufactured with new design revision are completely compatible with existing product from form, fit, functional, parametric, and quality performance perspectives.

Cypress also recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

Response Required:

No response is required.

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at pcn_adm@cypress.com.

Sincerely,

Cypress PCN Administration

Marketing Part Number
FM25V20A-PG