PCN Number: 202)220303002.1			PCN Date:		e:	March 04, 2022	
Title: Qualification of DM				10S6 as an additional Fab site option for select LBC8 devices					
Customer Contact:			PCN Manager		Dept:			Quality Services	
Proposed 1 st Ship Date:			Jun 4, 2022		Estimated Sample Availability:		ole	Date provided at sample request.	
Change Type:									
Assembly Site			Assembly Process				Assembly Materials		
Design	า		Electrical Specific		ication			Mechanical Specificati	
Test Site			Packing/Shipping/Labeling		ng		Test Process		
☐ Wafer Bump Site			Wafer Bump Material				Wafer Bump Process		
			Wafer Fab Materials				Wafe	Wafer Fab Process	
				Part number change					
PCN Details									

Description of Change:

Texas Instruments is pleased to announce the qualification of its DMOS6 fabrication facility as an additional Wafer Fab source for the selected devices listed in the "Product Affected" section.

С	urrent Fab Site	е	Additional Fab Site			
Current Fab Site			New Fab Site	Process	Wafer Diameter	
RFAB	LBC8	300mm	DMOS6	LBC8	300mm	

Qual details are provided in the Qual Data Section.

Reason for Change:

Continuity of supply.

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Changes to product identification resulting from this PCN:

Fab Site Information:

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
RFAB	RFB	USA	Richardson
DMOS6	DM6	USA	Dallas

Sample product shipping label (not actual product label)



(L)T0:³⁹750



(1P) SN74LS07NSR (D) 0336 31T)LOT: 3959047MLA 4W) TKY(1T) 7523483S12 (V) 0033317 (21L) CCO:USA (20L) CSO: SHE

Product Affected:

BQ27426YZFR BQ27426YZFT

Qualification Report

Approve Date 02-Mar-2022

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Name / Condition	Duration	Qual Device: BQ27426YZFR	QBS Product / Package Reference: CD3258A0YFPR (RFAB/CLARK)	QBS Product / Package Reference: <u>CD3258A1YFPR</u> (<u>RFAB/CLARK)</u>	QBS Process Reference: CD3258A0 (DMOS6/CLARK)	QBS Process Reference: CD3258A1 (DMOS6/CLARK)
ED	Electrical Characterization.	Per Datasheet Parameters	1/Pass	-	1/Pass	-	-
ELFR	Early Life Failure Rate, 125C	48 Hours	-	-	4/2000/0	-	4/2000/0
HAST	Biased HAST, 130C/85%RH	96 Hours	-	2/154/0	-	1/77/0	-
HBM	ESD - HBM	4000 V	1/3/0	-	-	-	-
CDM	ESD - CDM	1500 V	1/3/0	-	-	-	-
HTOL	Life Test, 140C	480 Hours	3/231/0	-	-	-	-
HTSL	High Temp. Storage Bake,150C	1000 Hours	-	1/77/0	1/77/0	1/77/0	1/77/0
HTSL	High Temp. Storage Bake, 170C	420 Hours	1/77/0	-	-	-	-
LU	Latch-up, Ta=Room	(per JESD78)	1/6/0	-	-	-	-
LU	Latch-up, Ta=High	(per JESD78)	1/6/0	-	-	-	-
TC	Temperature Cycle, -55/125C	700 Cycles	-	-	2/154/0	=	1/77/0
TX	Temperature Cycle, -65/150C	500 Cycles	1/77/0	-	-	-	-
UHAST	Unbiased HAST, 130C/85%RH	96 Hours	1/77/0	2/154/0	-	1/77/0	-

- QBS: Qual By Similarity
- Qual Device BQ27426YZFR is qualified at LEVEL1-260C
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free (SMT) and Green

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