



## Final Product Change Notification

202101040F01 : MKL28 Datasheet Update to Rev3

**Note:** This notice is NXP Company Proprietary.

**Issue Date:** Feb 08, 2021 **Effective date:** May 09, 2021

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### Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Test Process	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Equipment	<input type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Location	<input checked="" type="checkbox"/> Electrical spec./Test coverage
<input type="checkbox"/> Firmware	<input type="checkbox"/> Other			

## PCN Overview Description

NXP Semiconductors announces that the MKL28 Datasheet has been updated to Rev 3 (MKL28Z512Vxx7\_Rev3). The revision history included in the updated document provides a detailed description of the changes.

The updated MKL28 Datasheet can be found at below link after FPCN is effective:

[https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/kl-series-cortex-m0-plus/kinetis-kl28-72-96-mhz-512kb-flash-usb-ultra-low-power-microcontrollers-mcus-based-on-arm-cortex-m0-plus-core:KL28?fsp=1&tab=Documentation\\_Tab](https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/kl-series-cortex-m0-plus/kinetis-kl28-72-96-mhz-512kb-flash-usb-ultra-low-power-microcontrollers-mcus-based-on-arm-cortex-m0-plus-core:KL28?fsp=1&tab=Documentation_Tab)

A summary of the document changes is attached to this notice.

The spec of open loop total deviation of FIRC frequency is updated in Datasheet and MKL28 devices manufactured beginning with work week 22 of 2021 will follow the spec:

The manufacturing date code, xxxYYWWx, is marked on the last line of the package:

Where:

\* YY = Year (21 represents 2021, etc.)

\* WW = Work Week (22 represents week 22, etc.)

### Reason

The Datasheet has been revised to update the open loop total deviation of FIRC frequency.

### Identification of Affected Products

Product identification does not change

### Product Availability

### Sample Information

Samples are available upon request

### Production

Planned first shipment Jun 04, 2021

### Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

**Data Sheet Revision**

A new datasheet will be issued

**Disposition of Old Products**

Existing inventory will be shipped until depleted

## Timing and Logistics

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In compliance with JEDEC J-STD-046, your acknowledgement of this change is expected by Mar 10, 2021.

## Contact and Support

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For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

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NXP Quality Management Team.

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NXP Semiconductors  
High Tech Campus, 5656 AG Eindhoven, The Netherlands

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**Affected Part Numbers**

MKL28Z512VDC7

MKL28Z512VLL7R

MKL28Z512VLL7