

Vishay Siliconix

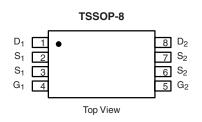
Dual N-Channel 2.5-V (G-S) MOSFET

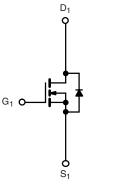
PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
20	0.045 at V _{GS} = 4.5 V	3.9		
	0.055 at V _{GS} = 3.0 V	3.5		
	0.065 at V _{GS} = 2.5 V	3.0		

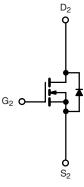
FEATURES

• Halogen-free









Ordering Information: Si6925ADQ-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	20		v	
Gate-Source Voltage		V _{GS}	± 12			
Continuous Drain Qurrent (T 150 °C)	T _A = 25 °C	- I _D	3.9	3.3	- 	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		3.1	2.6		
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	30		A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	1.0	0.72		
	T _A = 25 °C	- P _D	1.13	0.80	W	
Maximum Power Dissipation ^a	T _A = 70 °C		0.72	0.51	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 s	- R _{thJA} R _{thJF}	186	110		
Maximum Junction-to-Ambient ^a	Steady State		125	155	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State		65	85		

Notes:

a. Surface Mounted on FR4 board, $t \leq$ 10 s.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm.

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SPECIFICATIONS $T_J = 25 \circ 0$ Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static	<u> </u>				<u> </u>		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.6		1.8	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1		
		$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 70 ^{\circ}\text{C}$			15	μA	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}$	10			А	
Drain-Source On-State Resistance ^b		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3.9 \text{ A}$		0.035	0.045		
	R _{DS(on)}	$V_{GS} = 3.0 \text{ V}, \text{ I}_{D} = 3.5 \text{ A}$ 0.042			0.055	Ω	
		$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 3.0 \text{ A}$		0.050	0.065		
Forward Transconductance ^b	9 _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 3.9 \text{ A}$		14		S	
Diode Forward Voltage ^b	V _{SD}	I _S = 1.0 A, V _{GS} = 0 V		0.75	1.1	V	
Dynamic ^a			•	•			
Total Gate Charge	Qg			4.0	6		
Gate-Source Charge	Q _{gs}	$V_{DS} = 6 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 3.9 \text{ A}$		0.9		nC	
Gate-Drain Charge	Q _{gd}			1.0			
Gate Resistance	R _g			1.9		Ω	
Turn-On Delay Time	t _{d(on)}			40	60		
Rise Time	t _r	V_{DD} = 6 V, R_L = 6 Ω		50	75	ns	
Turn-Off Delay Time	t _{d(off)}	$\rm I_D \cong 1$ A, $\rm V_{GEN}$ = 4.5 V, $\rm R_g$ = 6 Ω		20	30		
Fall Time	t _f			10	20		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.0 A, dl/dt = 100 A/μs		20	40		

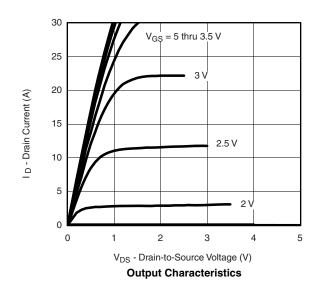
Notes:

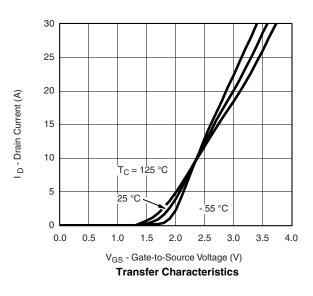
a. Guaranteed by design, not subject to production testing.

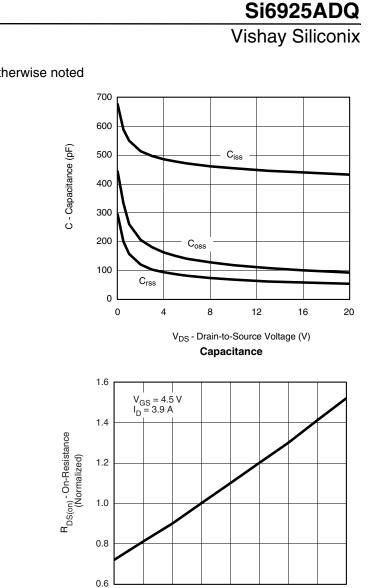
b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







T_J - Junction Temperature (°C) On-Resistance vs. Junction Temperature

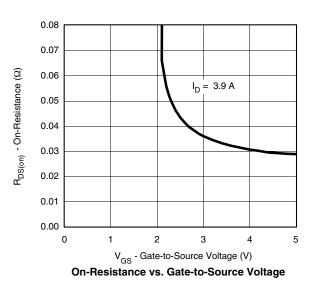
50

75

100

125

150



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

V_{GS} = 3.0 V

15

I_D - Drain Current (A)

On-Resistance vs. Drain Current

2

1

3

Q_q - Total Gate Charge (nC)

Gate Charge

T_J = 150 °C

0.6

V_{SD} - Source-to-Drain Voltage (V) Source-Drain Diode Forward Voltage

0.8

20

V_{GS} = 4.5 V

25

30

5

4

T_J = 25 °C

1.0

1.2

- 50

- 25

0

25

VISHAY

0.10

0.08

0.06

0.04

0.02

0.00

0

6

5

4

3

2

1

0 2

30

10

S - Source Current (A)

 $V_{GS} = 2.5 V$

5

 $V_{DS} = 6 V$ $I_{D} = 3.9 A$

10

 $R_{DS(on)}$ - On-Resistance ($\Omega)$

V_{GS} - Gate-to-Source Voltage (V)

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1

0.0

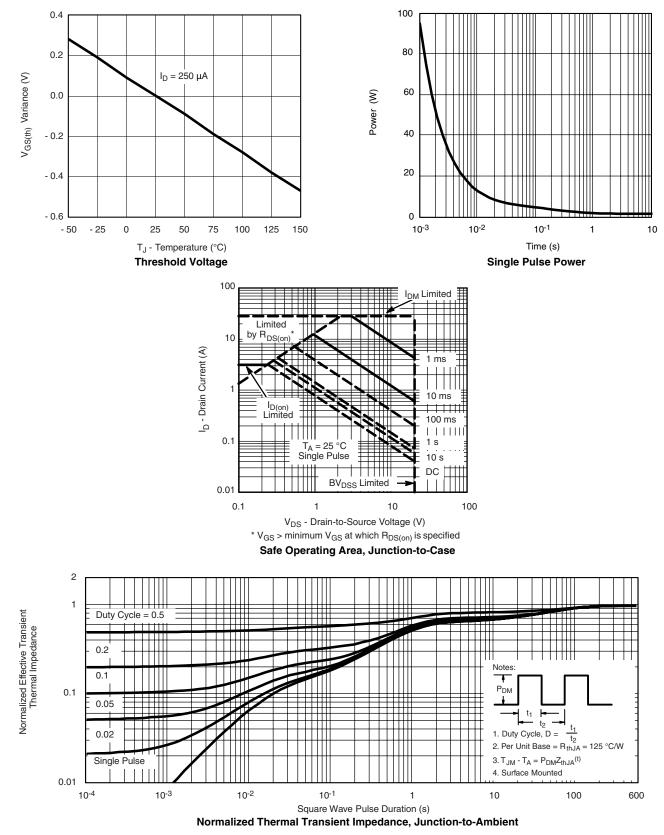
0.2

0.4

Si6925ADQ

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



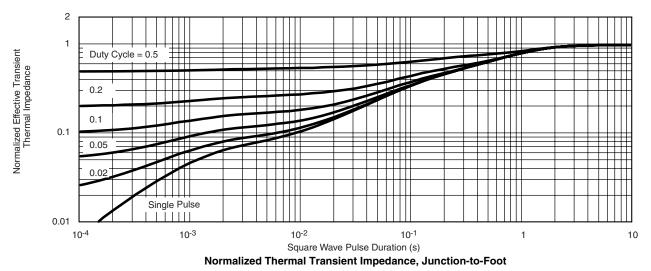
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Si6925ADQ

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72623.



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