



Customer Information Notification

201903028I

Issue Date: 19-Apr-2019
Effective Date: 20-Apr-2019

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QUALITY

Change Category

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| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input checked="" type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware | <input type="checkbox"/> Other | | | |

i.MXRT1050 Data Sheet Rev 1.3 Updates

Description

NXP Semiconductors announces a data sheet update for the i.MXRT1050 to revision 1.3. The revision history included in the updated documents provides a detailed description of the changes. Changes are summarized below.

Industrial Data Sheet:

1. Add one new 12x12 part number MIMXRT1051DVJ6B

Other changes for both industrial and consumer Data Sheet:

1. Updated the ADC numbers in the Figure 2, "i.MX RT1050 system block diagram"
2. Removed tamper detection from the Table 2, i.MX RT1050 modules list
3. Updated the value of JTAG_MOD in the Table 4, JTAG Controller interface summary
4. Changed 528 MHz PLL to System PLL in the Table 13, Low power mode current and power consumption and Table 16, System PLL's electrical parameters
5. Changed 480 MHz PLL to USB PLL in the Table 18, USB PLL's electrical parameters
6. Added the Figure 36, "Minimum Sample Time Vs Ras (Cas = 2pF)", Figure 37, "Minimum Sample Time Vs Ras (Cas = 5 pF)", and Figure 38, "Minimum Sample Time Vs Ras (Cas = 10 pF)" in the Section 4.8.2,

A/D converter

7. Updated Section 4.9.1, LPSPi timing parameters

The i.MXRT1050 data sheet revision 1.3 is attached to this notice, and can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-based-processors-and-mcus/i.mx-applications-processors/i.mx-rt-series/i.mx-rt1050-crossover-processor-with-arm-cortex-m7-core:i.MX-RT1050?tab=Documentation_Tab

Reason

The datasheet has been updated to correct errors and provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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Changed Orderable Part#	Changed Part 12NC	Changed Part Number	Changed Part Description	Package Outline	Package Name	Status	Product Line
MIMXRT1052DVL6A	935351124557	MIMXRT1052DVL6A	i.MXRT1050	SOT1546-2	LFBGA196	RFS	MCUs
MIMXRT1051DVL6B	935365839557	MIMXRT1051DVL6B	i.MXRT1050 A1	SOT1546-2	LFBGA196	RFS	MCUs
MIMXRT1051DVJ6B	935376003557	MIMXRT1051DVJ6B	i.MXRT1050 BGA12	SOT1968-1	LFBGA196	RFS	MCUs
MIMXRT1051CVL5B	935365838557	MIMXRT1051CVL5B	i.MXRT1050 A1	SOT1546-2	LFBGA196	RFS	MCUs
MIMXRT1052DVJ6B	935376005557	MIMXRT1052DVJ6B	i.MXRT1050 BGA12	SOT1968-1	LFBGA196	RFS	MCUs
MIMXRT1052DVL6B	935365842557	MIMXRT1052DVL6B	i.MXRT1050 A1	SOT1546-2	LFBGA196	RFS	MCUs
MIMXRT1052CVL5B	935365841557	MIMXRT1052CVL5B	i.MXRT1050 A1	SOT1546-2	LFBGA196	RFS	MCUs
MIMXRT1052CVJ5B	935376004557	MIMXRT1052CVJ5B	i.MXRT1050 BGA12	SOT1968-1	LFBGA196	RFS	MCUs
MIMXRT1052CVL5A	935351123557	MIMXRT1052CVL5A	i.MXRT1050	SOT1546-2	LFBGA196	RFS	MCUs