



# **Dual P-Channel 1.8-V (G-S) MOSFET**

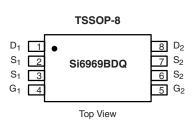
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
- 12	0.030 at $V_{GS} = -4.5 \text{ V}$	- 4.6		
	0.040 at V <sub>GS</sub> = - 2.5 V	- 3.8		
	0.055 at V <sub>GS</sub> = - 1.8 V	- 3.0		

#### **FEATURES**

- Halogen-free Option Available
- TrenchFET® Power MOSFETs

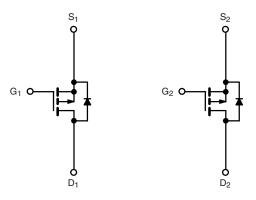






Ordering Information: Si6969BDQ-T1

Si6969BDQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T	A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 12		V
Gate-Source Voltage		V <sub>GS</sub>	± 8		
Continuous Dunis Comment /T 150 00\8	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 4.6	- 4.0	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 3.8	- 3.2	^
Pulsed Drain Current (10 μs Pulse Width)		I <sub>DM</sub>	- 30		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.0	- 0.7	
Mariana Barra Birata di ad	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.14	0.83	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		0.73	0.53	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum landing to Audienta	t ≤ 10 s	- R <sub>thJA</sub>	88	110	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		120	150	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	65	80	

#### Notes:

a. Surface Mounted on 1" x 1" FR4 board.

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply.

# Vishay Siliconix



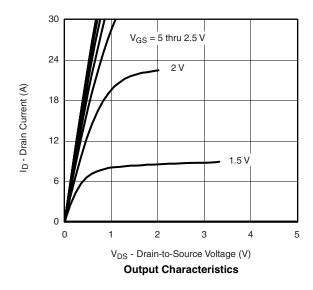
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static			•	•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 0.45		- 0.8	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 8 V		± 100	nA	
Zero Gate Voltage Drain Current		V <sub>DS</sub> = - 9.6 V, V <sub>GS</sub> = 0 V			- 1		
	I <sub>DSS</sub>	$V_{DS}$ = - 9.6 V, $V_{GS}$ = 0 V, $T_{J}$ = 70 °C			- 25	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> - 8 V, V <sub>GS</sub> = - 4.5 V	- 30			Α	
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = -4.5 \text{ V}, I_D = -4.6 \text{ A}$		0.024	0.030		
	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -3.8 \text{ A}$		0.031	0.040	0 Ω	
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 3.0 A		0.044	0.055		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 8 V, I <sub>D</sub> = - 4.6 A		18		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.25 A, V <sub>GS</sub> = 0 V		- 0.68	- 1.1	٧	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			16.5	25		
Gate-Source Charge	$Q_{gs}$	$Q_{gs}$ $V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -4.6 \text{ A}$		2		nC	
Gate-Drain Charge	$Q_{gd}$			4.7			
Turn-On Delay Time	t <sub>d(on)</sub>			20	40		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 6 V, $R_L$ = 6 $\Omega$		35	60		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 1.0 A, $V_{GEN}$ = - 4.5 V, $R_G$ = 6 $\Omega$		110	180	ns	
Fall Time	t <sub>f</sub>			90	150		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.25 A, dl/dt = 100 A/μs		100	200		

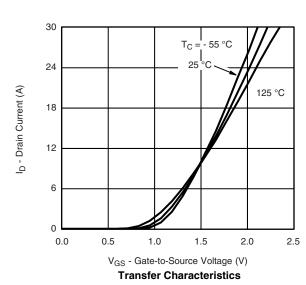
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

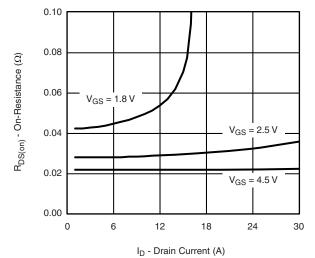




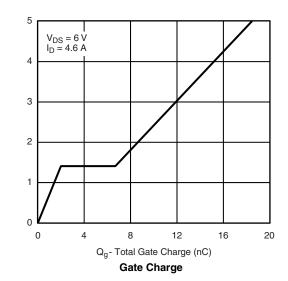


V<sub>GS</sub> - Gate-to-Source Voltage (V)

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On-Resistance vs. Drain Current



T<sub>J</sub> = 150 °C

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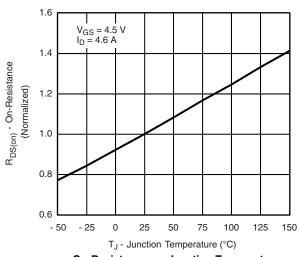
T<sub>J</sub> = 25 °C

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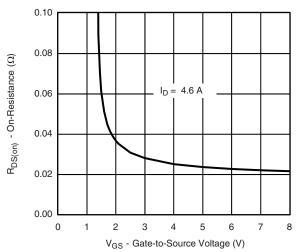
V<sub>SD</sub> - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

2500 2000 1500 1000 Crss Coss 500 Crss 0 2 4 6 8 10 12



On-Resistance vs. Junction Temperature



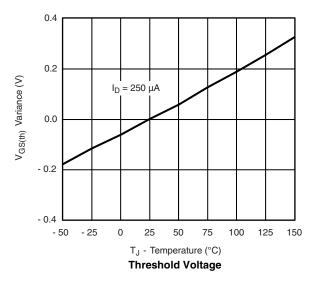
On-Resistance vs. Gate-to-Source Voltage

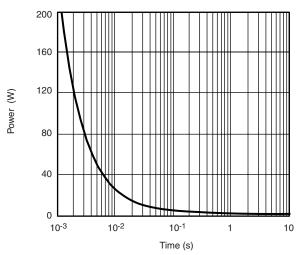
30

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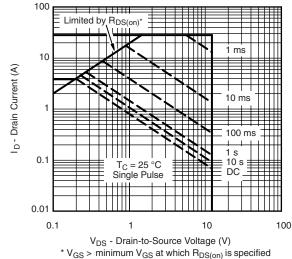
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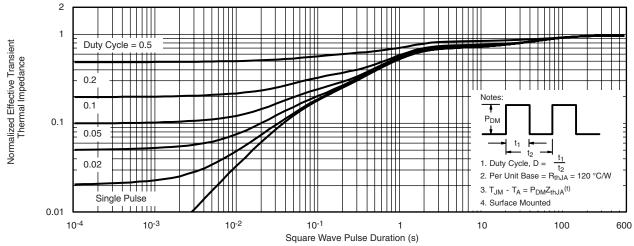




Single Pulse Power, Junction-to-Ambient



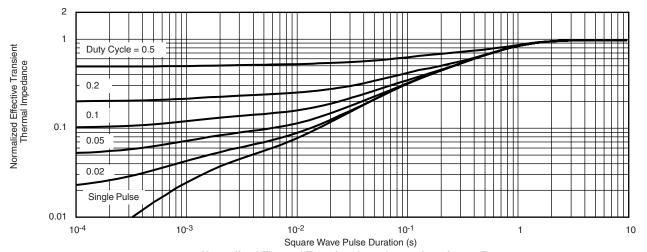
Safe Operating Area, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Ambient



## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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