



Product Change Notification / SYST-02VSSG610

Date:

07-Jul-2021

Product Category:

Ethernet Switches

PCN Type:

Document Change

Notification Subject:

Data Sheet - KSZ8795CLX Integrated 5-Port 10/100 Managed Ethernet Switch with Gigabit GMII/RG Document Revision

Affected CPNs:

[SYST-02VSSG610_Affected_CPN_07072021.pdf](#)
[SYST-02VSSG610_Affected_CPN_07072021.csv](#)

Notification Text:

SYST-02VSSG610

Microchip has released a new Product Documents for the KSZ8795CLX Integrated 5-Port 10/100 Managed Ethernet Switch with Gigabit GMII/RG of devices. If you are using one of these devices please read the document located at [KSZ8795CLX Integrated 5-Port 10/100 Managed Ethernet Switch with Gigabit GMII/RG](#).

Notification Status: Final

Description of Change:

- 1) Global. Updated Master/Slave terminology to reflect Host/ Client.
- 2) Section 3.5.1.1, "SPI Client Serial Bus Configuration". Updated maximum SPI bus frequency to 25 MHz.
- 3) Table 4-3. Updated Register 12 (0x0C): Global Control 10
- 4) Figure 7-9, Figure 7-10, Table 7-6. Updated both images and SPI Timing Parameters
- 5) Table 4-21. Updated Port_ACL_2 through Port_ACL_9, Layer 4
- 6) Table 3-15. Updated Destination for 0,1,x,x
- 7) Table 3-11. Updated default value for bit[3] of 01 Mode and bit[3] of 10 Mode.
- 8) Figure 3-5. Updated Receive Path of this figure

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 07 July 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[KSZ8795CLX Integrated 5-Port 10/100 Managed Ethernet Switch with Gigabit GMII/RG](#)

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Affected Catalog Part Numbers (CPN)

KSZ8795CLXCC

KSZ8795CLXIC