CMX869B

Low Power V.32 bis Modem

The CMX869B is a multi-standard modem for use in EPOS terminals and telephone based information and telemetry systems. The device provides the functions for a ITU V.32 bis automode modem or a V.22 bis, V.22, V.21 and Bell 202, Bell 103 compatible modem operating under external host control for EPOS and other proprietary protocols.

Features

- V.32 bis/V.32/V.22 bis/V.22 Automodem (14400, 12000, 9600, 7200, 4800, 2400, 1200 bps Duplex)

- V.2 bis/V.22 Manual Modem (2400, 1200 bps)

- V.23 (1200/75, 1200/120, 75, 1200 bps FSK)

- Bell 202 (1200/150), 1200/1200, 150, 1200 bps FSK)

- V.21 or Bell 103 (300/300 bps FSK)

- High Performance DTMF Modem

- Single/Dual Tones Transmit and Receive

- 'Powersave' Standby Mode

- Asynchronous, Synchronous and HDLC Modes

Applications

- EPOS Terminals

- Telephone Telemetry Systems

- Remote Utility Meter Reading

- Security Systems

- Industrial Control Systems

- Electronic Cash Terminals

- Pay-Phones

- Cable TV Set-top Boxes

Supply Requirement

- 3.0 to 3.6 V

The V.32 bis automode-modem provides 14400 bps operation with automatic fallback through to 4800 bps, retrain, rate re-negotiation and automatic detection of V.22 and V.22 bis modems. A high-quality DTMF decoder with excellent immunity to falsing on voice and a standard DTMF encoder are included. Alternatively, the device can transmit and detect user-programmed single and dual-tone signals, call progress signals or modem calling and answering tones. The CMX869B features a software controlled output to drive a hook switch relay and a ring detector block that also functions in powersave.

Line I/Os can be single-ended or differential and the line-output amplifier is capable of directly driving into a low-impedance transformer or opto-isolated DAA. The hybrid and gain control circuits are integrated on chip, requiring only passive external components to build a 2- or 4-wire line interface. Host control and data transfer is via CML's C-BUS serial interface.

An embedded USART accepts multi-format asynchronous data with V.14 supports or allows unformatted synchronous data or HDLC framed data to be received or transmitted. Data transfer can be either an 8– or 16-bit format.

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WHAT TO DO NEXT

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Developing proven high performance and field tested ASSP ICs, CML is helping engineers to cope with increasing
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